

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	32315	memory adj array	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:20
L2	40	1 with smart	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:21
L3	57577	dsp or (digital adj signal adj processor)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:21
L4	15365	CODEC	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:21
L5	3	MMRAM or (multi-media adj RAM)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:22
L6	14	2 and 3	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:22
L7	0	2 same 3	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:22
L8	4	2 and 4	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:22
L9	507	(711/104).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:23
L10	534	(711/115).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:23

L11	1686	(711/154).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:23
L12	1295	(711/167).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/19 20:23